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11042 U.S. PRO

# UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications  
under 37 CFR 1.53(b))

Title of Invention

Simulator Of Semiconductor Device Circuit  
Characteristic And Simulation Method

Named Inventor(s)

Tetsuya Yamaguchi

Attorney Docket

44471-264230 (13700)

Express Mail Label No.

EL910717696US

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## APPLICATION ELEMENTS

1. ☒ Fee Transmittal Form  
(Submit an original, and a duplicate for fee processing)
2. ☐ Applicant claims Small Entity status
3. ☒ Specification, Claims,  
and Abstract Total Pages 20
4. ☒ Drawings Total Sheets 11
5. Oath or Declaration Total Pages 1
  - a. ☒ Newly executed (original or copy)
  - b. ☐ Copy from prior application (37 CFR 1.63(d))  
(for continuation/divisional with Box 16  
completed)  
The entire disclosure of the prior application, from which  
a copy of the oath or declaration is supplied under Box  
5b, is considered as being part of the disclosure of the  
accompanying application and is hereby incorporated by  
reference therein.  
(i) ☐ DELETION OF INVENTOR(S)  
Signed statement attached deleting  
inventor(s) named in the prior  
application, see 37 CFR 1.63(d)(2)  
and 1.33(b).
6. ☐ Microfiche Computer Program (Appendix)

ADDRESS TO: Assistant Commissioner for Patents  
Box Patent Application  
Washington, D.C. 20231

## ACCOMPANYING APPLICATION PARTS

7. ☐ Nucleotide and/or Amino Acid Sequence  
Submission (if applicable, all necessary)
  - a. ☐ Computer Readable Copy
  - b. ☐ Paper Copy (identical to computer copy)
  - c. ☐ Statement verifying identity of  
above copies
8. ☒ Assignment:
  - a. ☒ Assignment Papers (cover sheet &  
document(s))
  - b. ☐ Assignment is of record in parent  
application No. \_\_\_\_\_
9. ☐ 37 CFR 3.73(b) Statement  
(when there is an assignee)  
☐ Power of Attorney by assignee
10. ☐ English Translation Document (if applicable)
11. ☐ Information Disclosure Statement (IDS) PTO-  
1449  
☐ Copies of IDS Citations
12. ☐ Preliminary Amendment
13. ☒ Return Receipt Postcard (MPEP 503)  
(Should be specifically itemized)
14. ☐ Certified Copy of Priority Document(s)
15. ☐ Other: Checks For \$710.00; \$40.00

16. If a **CONTINUING APPLICATION**, check appropriate box and supply the requisite information:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No: \_\_\_\_\_  
Recite complete dependency back to first parent application: \_\_\_\_\_

17. CORRESPONDENCE ADDRESS:

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By: Roger T. Frost Reg. No. 22,176  
Date: September 26, 2001

**FEE TRANSMITTAL**Attorney Docket No. **44471-264230 (13700)**

This sheet accompanies a patent application transmittal for the following application:

Inventor(s): **Tetsuya Yamaguchi**  
Filing Date: **September 26, 2001**  
Title: **Simulator Of Semiconductor Device Circuit  
Characteristic And Simulation Method**

The filing fee is calculated as shown below:

1. **FILING FEE:**

SMALL ENTITY			LARGE ENTITY	
FOR:	FEE	FEE PAID	FEE	FEE PAID
<input checked="" type="checkbox"/> UTILITY FILING FEE	\$355	\$	\$710	\$710
<input type="checkbox"/> DESIGN FILING FEE	\$160		\$320	
<input type="checkbox"/> PLANT FILING FEE	\$245		\$490	
<input type="checkbox"/> REISSUE FILING FEE	\$355		\$710	
<input type="checkbox"/> PROVISIONAL FILING FEE	\$75		\$150	
SUBTOTAL (1)		\$		\$710

2. **CLAIMS:**

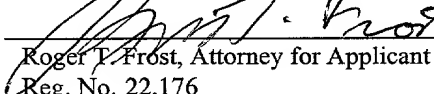
SMALL ENTITY				LARGE ENTITY		
FOR:	NO. FILED	NO. EXTRA	RATE	FEE	RATE	FEE
TOTAL CLAIMS	18 - 20 =	0	x 9 =		x 18 =	\$0
INDEP. CLAIMS	3 - 3 =	0	x 40 =		x 80 =	\$0
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIM PRESENTED			+135 =		+270 =	
SUBTOTAL (2)				\$		\$0

3. **ADDITIONAL FEES:**

SMALL ENTITY			LARGE ENTITY	
FOR:	FEE	FEE PAID	FEE	FEE PAID
<input type="checkbox"/> LATE FILING, FEE OR OATH	\$65		\$	\$
<input type="checkbox"/> NON-ENGLISH SPECIFICATION	\$130		\$	
<input type="checkbox"/> OTHER				
SUBTOTAL (3)		\$		\$

**TOTAL FILING FEES: \$710**A check is enclosed for the total amount: **\$710**☒ Charge any additional fees required under 37 C.F.R. 1.16 or 1.17 to Deposit Account 11-0855.

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By:   
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